



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1459  
Alexandria, Virginia 22313-1459  
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/740,634	12/18/2000	Wilhelm Schmid	GR 98 P 1916 P	4357

7590 12/02/2003  
LERNER AND GREENBERG, P.A.  
POST OFFICE BOX 2480  
HOLLYWOOD, FL 33022-2480

EXAMINER

PATEL, PARESH H

ART UNIT PAPER NUMBER

2829

DATE MAILED: 12/02/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

09/740,634

Applicant(s)

SCHMID ET AL.

Examiner

Paresh Patel

Art Unit

2829

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 29 August 2003.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-12, 14-18 is/are pending in the application.
- 4a) Of the above claim(s) 5, 6, 8 and 14-16 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-4, 7, 9-12, 17 and 18 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 29 August 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. §§ 119 and 120

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☒ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 13) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application) since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.
- a) ☐ The translation of the foreign language provisional application has been received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121 since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.

## Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413) Paper No(s) \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other:

## DETAILED ACTION

### *Election/Restrictions*

1. This application contains claims 5-6, 8 and 14-16 are drawn to an invention nonelected with traverse in Paper No. 8. A complete reply to the final rejection must include cancellation of nonelected claims or other appropriate action (37 CFR 1.144) See MPEP § 821.01.

### *Response to Arguments*

2. Applicant's arguments filed 08/29/2003 have been fully considered but they are not persuasive. At page 18 applicants argues that "*Palagonia* does not show at least one connecting contact point externally accessible for monitoring electrical signals **for test purpose** as recited in claims 1 and 17 of the instant application". Examiner disagrees because, 1) claim 1 is amended to include above limitation, *Palagonia* at lines 56-62 on column 3 and 11-16 of column 4 suggests the testing of die 40, and 2) claim 17 does not recite those limitation and hence it is not valid argument for claim 17. Applicant have amended claim 18 (which depend from claim 17) to include above limitation. *Palagonia* at lines 56-62 on column 3 and 11-16 of column 4 suggests the testing of the die 40 **where terminals 42A is externally available to interposer 10**.

3. **Claim 13 is cancelled.**

***Claim Rejections - 35 USC § 102***

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claims 1-4, 7, 9-12 and 17-18 are rejected under 35 U.S.C. 102(b) as being anticipated by Palagonia (US 5895978).

Regarding claims 1 and 17, Palagonia in fig. 2 discloses: an integrated circuit component [40], comprising:

a first plurality of circuit points (plurality of circuit points of claim 1) [46 with 42A, lines 55-62 of column 3 and lines 11-16 of column 4] not being externally accessible (and monitored for test purpose for claim 1);

a second plurality of circuit points [46 with 44] not being externally accessible and;

a first connecting contact point (at least one connecting contact point of claim 1) [42A] being externally accessible;

a second connecting contact point [next to 42A] being externally accessible;

a first multiplexer (a multiplexer of claim 1) [MUX of 46 with 42A] having an output [output of MUX to 42A] connected to said first connecting contact point (at least one connecting contact point of claim 1) and having a plurality of inputs [from 46], each

one of said plurality of inputs being connected [via 52] to a respective one of said first plurality of circuit points (plurality of circuit points) [46].

a second multiplexer [MUX 44] having an output [output of MUX 44] connected to said second connecting contact point and having a plurality of inputs [from 46 or I/O], each one of said plurality of inputs being connected [via 52] to a respective one of said second plurality of circuit points [46 with 44].

Regarding claim 2 (currently amended) Palagonia discloses:

said at least one connecting contact point is one of a plurality of connecting contact points [plurality of 42A for each 26A];

reference signal [from 42B to 48 and then MUX of 46] is selectively applied to one of said plurality of connecting contact points and passed on via a route[50, 52] within the integrated circuit component to one of said plurality of circuit points that are not externally to one of said accessible; and

said electrical signals [output of 46] can be selectively passed on via routes within the integrated circuit component from said plurality of circuit points that are not externally accessible to said plurality points other than said one of said plurality of connecting contact points [42A].

Regarding claim 3 (currently amended) Palagonia discloses: said plurality of connecting exactly two connecting contact points [pad 42A and pad connected to 44 near 42A].

Regarding claim 4 (currently amended) Palagonia discloses:

said electrical signals are internal chip signals [I/O function signals of 40] in the integrated circuit component [40]; and

reference signals [from 42B] and said electrical signals [from 42A] can be selectively passed on to said at least one connecting contact point.

Regarding claim 7 (currently amended) Palagonia discloses: integrated circuit component further comprising a second multiplexing circuit [44].

Regarding claim 9 (currently amended) Palagonia discloses: wherein said multiplexing circuit is programmably controlled to predetermine selective passing on of the electrical signals to said at least one connecting contact point [using 48 and MUX of 46].

Regarding claim 10 (currently amended) Palagonia discloses: said at least one connecting contact point [42A because it input/output function signal of die 40] can be selectively [using MUX of 46] used in an opposite operating direction for inputting signals to said plurality of circuit points that are not externally accessible.

Regarding claim 11 (currently amended) Palagonia discloses: said at least connecting contact point is connected to a component tester [inherent to testing as further defined at lines 56-62 of column 3 and lines 11-16 of column 4] for analyzing the electrical signals.

Regarding claim 12 (currently amended) Palagonia discloses: said at least one connecting contact point is connected to a system analyzer and an application of said system analyzer analyzes the electrical signals of at least some of said plurality of

circuit points [inherent to testing and cost as further defined at lines 56-62 of column 3 and lines 11-16 of column 4].

Regarding claim 18 (currently amended) Palagonia discloses:

said first connecting contact point receives and passes on a reference signal [signal from 48] via a route [50] within the integrated circuit component to one of said first plurality of circuit points that are not externally accessible; and

said second plurality of circuit points of the integrated circuit are providing electrical signals [output from 46 to 42A] to be monitored for test purposes [lines 56-62 of column 3 and lines 11-16 of column 4], one of said second plurality of circuit points being connected to said second connecting contact points.

### ***Conclusion***

6. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of

Art Unit: 2829

the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Paresh Patel whose telephone number is 703-306-5859. The examiner can normally be reached on 8:00 to 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamand Cuneo can be reached on 703-308-1233. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

Paresh Patel  
Nov. 21, 2003

*David A. Zaneke*  
David A. Zaneke  
Primary Examiner  
11/26/03